

Vlsi Algorithms And Architectures

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IEEE TRANSACTIONS ON MULTIMEDIA, VOL. 16, NO. 8, DECEMBER 2014
2323 A New Reference Frame Recompression Algorithm and Its VLSI Architecture for UHDTV Video Codec Li
http://www.academia.edu/13795558/A_New_Reference_Frame_Recompression_Algorithm_and_Its_VLSI_Architecture_for_UHDTV_Video_Codec

VLSI Architectures for Modern Error-Correcting -

VLSI Architectures for Modern Error-Correcting Codes Kotter's Interpolation Algorithm and Architectures. Her research focuses on VLSI architecture design
<http://www.tandf.net/books/details/9781482229646/>

Algorithms, Complexity Analysis and VLSI -

Algorithms, Complexity Analysis and VLSI Architectures for MPEG-4 Motion Estimation [Peter M. Kuhn] on Amazon.com. *FREE* shipping on qualifying offers. MPEG-4 is the
<http://www.amazon.com/Algorithms-Complexity-Analysis->

[Architectures-Estimation/dp/0792385160](https://www.scribd.com/doc/154492317/Computer-Arithmetic-Principles-Architectures-and-VLSI-Design)

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VLSI algorithms and architecture for relational -

The use of a special purpose VLSI chip for relational operations is proposed. The chip is structured like a tree with processors at the nodes, called TOP (Tree of

<http://link.springer.com/article/10.1007/BF02576201>

VLSI algorithms and architectures. Fundamentals -

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Tree Search Based MIMO Detectors: Algorithms and -

Tree Search Based MIMO Detectors: Algorithms and VLSI Architectures. By admin in on February 23, 2012 7:09 am.
Location: 2111 Engineering Hall

<http://www.cecs.uci.edu/event/tree-search-based-mimo-detectors-algorithms-and-vlsi-architectures/>

VLSI Computation Lab. (VCL) - UC Davis Department of -

We are working on LDPC algorithms, architectures, IEEE Transactions on Very Large Scale Integration Computing resources within the VLSI Computation Lab

<http://web.ece.ucdavis.edu/vcl/>

VLSI Algorithm, Architecture and Implementation -

CiteSeerX - Scientific documents that cite the following paper: VLSI Algorithm, Architecture and Implementation for High-Speed Lempel-Ziv Data Compression

<http://citeseerx.ist.psu.edu/showciting?cid=2213936>

From Algorithms to Architectures - Top-Down VLSI -

Devising a suitable circuit architecture for a set of signal or data processing applications is one of the most exciting challenges for any VLSI designer. Depen

<http://www.sciencedirect.com/science/article/pii/B9780128007303000034>

VLSI and Computer Architecture - UB Computer -

VLSI and Computer Architecture. Hardware and software issues from the circuit to the system have three main foci: Very Large Scale Integration (VLSI) circuits and

<http://www.cse.buffalo.edu/research/areas/vlsi.php>

VLSI Architecture for a Flexible Motion -

usually been paid for a loss of efficiency. In this paper, we propose algorithm and flexible hard-ware architecture, which supports more than one algo-

http://www.cecs.uci.edu/~papers/compendium94-03/papers/2002/aspdac02/pdffiles/04c_4.pdf

VLSI Architectures for Image Interpolation: A -

The goal of this work is to analyze the different very large scale integration enhanced interpolation architectures. The algorithms are implemented for

<http://www.hindawi.com/journals/vlsi/2014/872501/>

Anna unviersity VLSI Design Syllabus -

To understand the various VLSI architectures for digital 3. Christophe Bobda, Introduction to Reconfigurable Computing Architectures, Algorithms

<https://www.scribd.com/doc/273374836/Anna-unviersity-VLSI-Design-Syllabus>

Algorithms and Parallel VLSI Architectures III, -

Elsevier Store: Algorithms and Parallel VLSI Architectures III, 1st Edition from M. Moonen, F. Catthoor. ISBN-9780444821065, Printbook , Release Date: 1995

<http://store.elsevier.com/Algorithms-and-Parallel-VLSI-Architectures-III/isbn-9780444821065/>

Entropy Coders of the H.264/AVC Standard - -

This book presents a collection of algorithms and VLSI architectures of entropy (or statistical) codecs of recent video compression standards, with focus

<http://www.springer.com/us/book/9783642147029>

VLSI algorithms and architectures for complex -

VLSI algorithms and architectures for complex householder transformation with applications to array processing

<http://link.springer.com/article/10.1007/BF00930618>

CORDIC Architectures: A Survey -

In the last decade, CORDIC algorithm has drawn wide attention from academia and industry for various applications such as DSP, biomedical signal processing, software

<http://www.hindawi.com/journals/vlsi/2010/794891/>

Efficient Algorithms and Architectures for -

Efficient Algorithms and Architectures for Multiuser Channel Estimation and Detection in Wireless iterative scheme with a simple fixed-point VLSI architecture.

<http://dsp.rice.edu/publications/efficient-algorithms-and-architectures-multiuser-channel-estimation-and-detection-wirel>

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3 - Mapping Algorithms onto VLSI Architectures -

Methods are described which map a given algorithm onto a circuit. In particular, the application domain is signal and image processing, the target algorithms ar

<http://www.sciencedirect.com/science/article/pii/B9780444887900500095>